

**S/N 10/612793**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Arup Bhattacharyya

Examiner: Fazli Erdem

Serial No.: 10/612793

Group Art Unit: 2826

Filed: July 2, 2003

Docket: 1303.111US1

Title: HIGH-PERFORMANCE ONE-TRANSISTOR MEMORY CELL

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Restriction Requirement mailed February 19, 2004, Applicant elects, without traverse, Group II which includes claims 1-79. Applicant withdraws remaining claims 80-100 (Group I) without prejudice, and reserves the right to reintroduce them for reconsideration in this application upon the allowance of a linking claim or to reintroduce them in a divisional application at a later date. Applicant respectfully asserts that at least claim 1 is a linking claim in so far as it is a claim to the product linking a process of making and a use. (MPEP §809.03).


The Examiner is invited to contact Applicant's Representatives at the below-listed telephone number if there are any questions regarding this Response or if prosecution of this application may be assisted thereby.

Respectfully submitted,

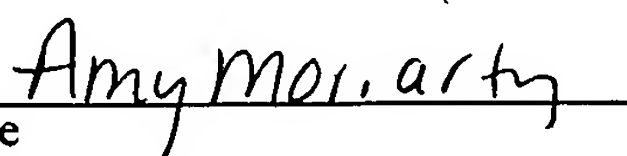
ARUP BHATTACHARYYA

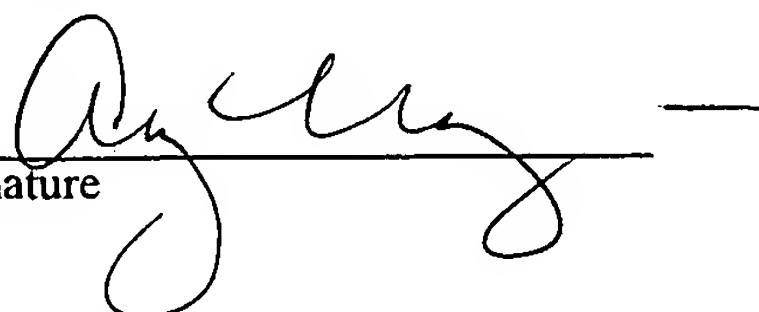
By Applicant's Representatives,

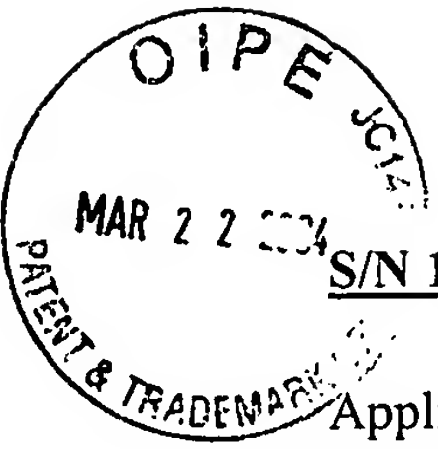
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
P.O. Box 2938  
Minneapolis, MN 55402  
(612) 373-6960

Date 3-19-04 By   
Marvin L. Beekman  
Reg. No. 38,377

**CERTIFICATE UNDER 37 CFR 1.8:** The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 19th day of March, 2004.

  
Name

  
Signature



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COMMUNICATION CONCERNING RELATED APPLICATION(S)

Commissioner for Patents.  
P.O. Box 1450  
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

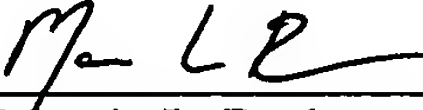
<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
10/232855	August 30, 2002	1303.072US1	GATED LATERAL THYRISTOR-BASED RANDOM ACCESS MEMORY CELL (GLTRAM)
10/232848	August 30, 2002	1303.073US1	ONE-DEVICE NON-VOLATILE RANDOM ACCESS MEMORY CELL
10/232846	August 30, 2002	1303.080US1	ONE TRANSISTOR SOI NON-VOLATILE RANDOM ACCESS MEMORY CELL
10/425483	April 29, 2003	1303.105US1	ONE TRANSISTOR SOI NON-VOLATILE RANDOM ACCESS MEMORY CELL

Respectfully submitted,

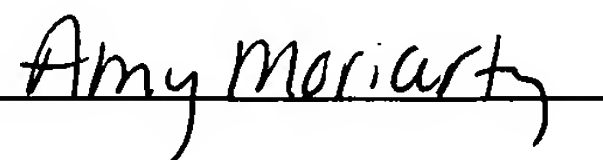
ARUP BHATTACHARYYA

By Applicant's Representatives,  
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
P.O. Box 2938  
Minneapolis, MN 55402  
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Date 3-19-04

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Name 

Signature 